TIFICATE OF MAILING

hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date March 1, 2005

Lutanande

PATENT 681331-85

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

ALAIN CHAPUIS

Serial No.:

10/724,509

Filed:

November 26, 2003

Examiner:

Art Unit:

2816

Title: ADAPTIVE DELAY CONTROL

CIRCUIT FOR SWITCHED MODE POWER

SUPPLY

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.56, Applicant submits herewith the attached Information Disclosure Statement Form PTO-1449 in connection with the above-identified patent application along with a copy of each reference (30). The statement is not a representation that all of the information cited is necessarily effective as prior art against the application.

Date: March 1, 2005

Respectfully submitted,

Brian M. Berliner

Attorney for Applicant Registration No. 34,549

O'MELVENY & MYERS LLP

400 So. Hope Street Los Angeles, CA 90071-2899 Telephone: (213) 430-6000

LA2:753238.1

FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. 681331-85 SERIAL NO. 10/724,509
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT: Alain CHAPUIS MAR 0 4 2005
	FILING DATE: November 26, 2003 GROUP: 2816

Reference Designation

U.S. Patents

\ Examiner					Filing Date Subclass if
Initial	Document Number	Date	Name	Class	appropriate
	AA 0027101	02/12/04	Vinciarelli		
	AB 0033152	10/25/01	Pohlman et al.		
	AC 0073347	06/13/02	Zafarana et al.		
	AD 0122429	07/03/03	Zhang et al.		
	AE 0142513	07/31/03	Vinciarelli		
	AF 0201761	10/30/03	Harris		
	AG 3,660,672	05/02/72	Berger et al.		
	AH 4,194,147	03/18/80	Payne et al.		
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	AV "33702 Microprocessor Power		oducts MC33702 Fact Sheet; Motoro		
	Supply (3.0A)		a/ Power Management Switching; pg		
-	Supply (5.0A)	Digital una	a rower management Switching; pg	S 1-4	
	AW "Motorola Switch Mode Power		Inc. 2002; digital dna; Analog Marke		
	Supply With Multiple Linear		l 1/2002; 33394; Multi-Output Power	•	
	Regulators And High Speed CAN	Supply Se	miconductor Technical Data		
	Transceiver"				
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	For Networking Applications";		ey Paris, France; Motorola digital dn		
	Presented by Luc Darmon	www.moto	orola.com/sndf; pgs. 1-26		
	AY "Preliminary Information 1.5 A	33701; Po	wer Supply Integrated Circuit; Motor	rola	
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	Linear Regulator		/D Rev. 1.0, 05/2003; Motorola digit		
	_	dna; pgs. 1			
EXAMINER			DATE CONSIDERED		
			2111 CONGIDENCE		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 of 2

FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. 681331-85	SERIAL NO. 10/724,509
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT	APPLICANT: Alain CHAPUIS	
(USE SEVERAL SHEETS IF NECESSARY)		
	FILING DATE: November 26, 2003	GROUP: 2816

↑ Reference Designation

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	AZ "The 1 2-C Bus Specification"	UTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) Version 2.1; January 2000; document order number: 9398 393 40011; Phillips Semiconductors; pgs. 1- 46
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	BB "KEKB Power Supply Interface Controller Module" by A. Akiyama, T. Nakamura, M. Yoshida, T. Kubo, N. Yamamoto and T. Katoh	KEK, High Energy Accelerator Research Organization, 1-1 Ohio, Tsukuba 305, Japan
	BC "Magnet Power Supply Control System KEKB Accelerators" by T.T. Nakamura, A. Akiyama, T. Katoh, Ta. Kubo, N. Yamamoto, M. Yoshida, KEK, Tsukuba, Japan	International Conference On Accelerator And Large Experimental Physics Control Systems, 1999, Trieste, Italy, pgs. 406 - 408
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EXAMINER		DATE CONSIDERED

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